

Status of Claims

Claims 1, 3-6, 8 and 9 are pending.

Claims 1, 3-6, 8 and 9 stand finally rejected.

Remarks/Arguments

Reconsideration of this application is respectfully requested.

Claims 1, 3-6, 8 and 9 stand rejected under 35 U.S.C. 103(a) as being unpatentable over Swenson (United States Patent No. 5,926,120) in view of Dally (United States Patent No. 6,269,435). Applicant traverses these rejections for at least the following reasons.

1. The Cited Art Fails to Teach Each of the Claim Limitations

To establish a *prima facie* case of obviousness, all of the recited claim limitations must be taught or suggested in the prior art. *See, MPEP 2143.03; see also, In re. Royka, 490 F.2d 981, 180 USPQ 580 (CCPA 1974).*

Applicant submits the cited art fails to teach each of the limitations of any of the pending claims, for at least the following reasons.

Turning first to Claim 1, it recites, in part:

A serial compressed bus interface, comprising:

a serial to parallel converter having a single serial data input line adapted to receive time-division multiplexed serial data from a plurality of data sources, and having a plurality of parallel output lines for providing thereon a packet of said time-division multiplexed serial data in parallel form to one of a plurality of devices associated with data applications.
(emphasis added)

Thus, Claim 1 calls for a serial to parallel converter having: (1) a single serial data input line that receives time-division multiplexed serial data from a plurality of data sources, and (2) a plurality of parallel output lines that provide